

-	7	audio and audio with record\$4 and input with microphone and first with input\$4 and second with input\$4 and compress\$4 and de\$compress\$4 and conver\$5 with analog with digital and conver\$5 with "a/d" and conver\$5 with "d/a" and switch\$4 and switch\$4 with digital\$4 and switch\$4 with analog not sony.as.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/03/17 14:26
-	7	audio and audio with record\$4 and input with (microphone mike) and first with input\$4 and second with input\$4 and compress\$4 and de\$compress\$4 and conver\$5 with analog with digital and conver\$5 with "a/d" and conver\$5 with "d/a" and switch\$4 and switch\$4 with digital\$4 and switch\$4 with analog not sony.as.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/03/17 15:01
-	0	first adj compress\$4 and second adj compress\$4 and memory adj (device card unit) and first adj memory and second adj memory and signal and controller and (determin\$4 identif\$4) with memory and record\$4 and audio and input\$4 with audio	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/03/17 15:01
-	1	first adj compress\$4 and second adj compress\$4 and memory adj (device card unit) and first adj memory and second adj memory and signal and controller and (determin\$4 identif\$4) with memory and record\$4 and audio and input\$4 same audio	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/03/17 15:01
-	7	audio and audio with record\$4 and input with (microphone mike) and first with input\$4 and second with input\$4 and compress\$4 and de\$compress\$4 and conver\$5 with analog with digital and conver\$5 with "a/d" and conver\$5 with "d/a" and switch\$4 and switch\$4 with digital\$4 and switch\$4 with analog not sony.as. and input\$4 with audio	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/03/17 15:02
-	81	copy\$right with compl\$6 and 713/\$.cccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/03/17 16:20
-	55840	(memory chip smart micro\$process\$4) near card	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:24
-	52804	(memory chip smart micro\$process\$4) adj card	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:24
-	6201	(memory chip smart micro\$process\$4) near card and signal with process\$4 with circuit\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:25
-	4	(memory chip smart micro\$process\$4) near card and signal with process\$4 with circuit\$4 with copy\$right\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:25
-	3	(memory chip smart micro\$process\$4) near card and signal with process\$4 with circuit\$4 with copy\$right\$4 with protect\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:26
-	230	(memory chip smart micro\$process\$4) near card and signal with process\$4 with circuit\$4 and copy\$right\$4 with protect\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:28
-	3	(memory chip smart micro\$process\$4) near card and (signal with process\$4 with circuit\$4) same copy\$right\$4 with protect\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:27

-	21	(memory chip smart micro\$process\$4) near card and signal with process\$4 with circuit\$4 and copy\$right\$4 with protect\$4 with signal\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:29
-	17	(memory chip smart micro\$process\$4) near card and signal with process\$4 with circuit\$4 and copy\$right\$4 with protect\$4 with signal\$4 not sony.as. not utsumi.in.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/11/10 10:30